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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/709,142	11/08/2000	Francis James Canova JR.	PALM-3530.P	3247

7590 05/11/2004  
Wagner Murabito & Hao LLP  
Two North Market Street  
Third Floor  
San Jose, CA 95113

EXAMINER

CASCHERA, ANTONIO A

ART UNIT PAPER NUMBER

2676

DATE MAILED: 05/11/2004

18

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/709,142

Applicant(s)

CANOVA ET AL.

Examiner

Antonio A Caschera

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 24 February 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 29 September 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |   |   |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)  | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date <u>16</u> . | 6) <input type="checkbox"/> Other: _____  |

**DETAILED ACTION**

***Continued Examination Under 37 CFR 1.114***

1. Receipt is acknowledged of a request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e) and a submission, filed on 2/24/2004.

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1, 5-7 and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hashimoto (U.S. Patent 5,784,132) in view of Taniguchi (U.S. Patent 4,824,212).

In reference to claim 1, Hashimoto discloses a display device comprising a panel structure housing pixel electrodes laid out in a matrix form (see column 1, lines 14-21 and prior art Figure 8). Hashimoto also discloses a drive substrate which is used to switch the pixel electrodes individually (see column 1, lines 17-21). Note, the office interprets the drive substrate "switching" process to be functionally equivalent to applicant's claimed light modulation (see lines 3-4 of claim 1) as the drive substrate controls the electric position (On/Off) of each pixel. Hashimoto also discloses dummy pixels arranged in positions so as to surround the effective display pixels (see column 5, lines 64-66) which the office interprets as equivalent to a pixel border having a predetermined width as the specific width is a matter of design choice as

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preferred by the designer and which best suits the application. Hashimoto discloses each dummy pixel made up of the same contents as an effective display pixel however the dummy pixels are not provided with a pixel electrode (#4 of Figure 4) (see column 6, lines 47-48 and Figure 4 where dummy pixel lacks pixel electrode as seen in #4 of right most pixel (effective display pixel)). Note, the pixel electrode of Hashimoto is seen to be functionally equivalent to an active element as the pixel electrode may be switched to illuminate the pixel. Hashimoto does not explicitly disclose the dummy pixels allowing light to pass through to improve contrast of edge-displayed images, however Taniguchi does. Taniguchi discloses an LCD device comprising two display region types, a display region and a non-display region (see lines 1-8 of abstract). Taniguchi discloses independently controlling elements of the non-display region, from those in the effective display region, to make the non-display region bright or dark (see column 2, lines 58-64). Note, the office interprets the elements making up the non-display region of Taniguchi functionally equivalent to the dummy pixels of applicant's claim. Taniguchi also discloses lighting the non-display region, either bright or dark, to create a distinguishable difference between the effective display and non-display regions (see column 2, lines 65-68). Note, the office interprets such a distinguishable difference functionally equivalent to contrast. Neither Hashimoto nor Taniguchi explicitly disclose generating an image wherein the image is representative of information stored in a frame buffer memory as a frame buffer is well known in the art to store frames of graphic data, known to make up graphic images or video, that are waiting to be displayed onto a display screen. It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement a frame buffer memory for storing information of an image to be displayed as such a technique is well known in the art

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(Official Notice). Further note, neither Taniguchi nor Hashimoto explicitly disclose the dummy pixels being not capable of modification however, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the controlling of dummy pixels in Taniguchi and Hashimoto to prohibit change in dummy pixel output thus making the dummy pixels not capable of modification as such processing could easily be implemented in software, instructing pixel drivers to not change dummy pixel output states. Further, any pixel in the LCD display art can be configured to not be capable of modification if the driving software is configured to not modify the pixel. It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement the dummy pixel display techniques of Hashimoto with the illuminating non-display region of the display of Taniguchi in order to create legible characters on a display device by implementing the rendering of, normally illegible characters, varying the illumination of the boundary of the effective display region surrounding the characters (see columns 1-2, lines 66-13 of Taniguchi).

In reference to claim 5, Hashimoto and Taniguchi disclose all of the claim limitations as applied to claim 1 above however neither Hashimoto nor Taniguchi explicitly disclose the predetermined width of the pixel border to be two pixels. At the time the invention was made, it would have been obvious to one of ordinary skill in the art to implement the predetermined border width of two pixels. Applicant has not disclosed that a border width of two pixels provides an advantage, is used for a particular purpose, or solves a stated problem. One of ordinary skill in the art, furthermore, would have expected Applicant's invention to perform equally well with any sized pixel border, suited for the application, because adjusting the correct dummy pixel border, to reduce contrast between edge adjacent text/images, may require different

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sized pixel borders depending upon different sized displays, font sizes or other device limitations. Therefore, it would have been obvious to one of ordinary skill in this art to modify Hashimoto to obtain the invention as specified in claim 5.

In reference to claim 6, Hashimoto and Taniguchi disclose all of the claim limitations as applied to claim 1 above however neither Hashimoto nor Taniguchi explicitly disclose the matrix display comprising 160 rows and 160 columns of pixels. At the time the invention was made, it would have been obvious to one of ordinary skill in the art to implement the display device of Hashimoto comprising 160 rows and 160 columns. Applicant has not disclosed that such a limitation provides an advantage, is used for a particular purpose, or solves a stated problem. One of ordinary skill in the art, furthermore, would have expected Applicant's invention to perform equally well with the display device of Hashimoto because such a limitation of size of display provides no immediate criticality when viewing the invention as a whole. Further, if the scope of the invention were geared towards a portable electronic device, it would have been obvious to create the display screen with a smaller size than rather the size of a desktop computer display. Therefore, it would have been obvious to one of ordinary skill in this art to modify Hashimoto to obtain the invention as specified in claim 6. Again, the office interprets such a limitation as a matter of design choice as preferred by the designer and which best suits the application.

In reference to claim 7, Hashimoto and Taniguchi disclose all of the claim limitations as applied to claim 1 above in addition, Hashimoto discloses the pixel electrodes laid out in matrix form with a plurality of thin film transistors (see column 1, lines 19-22).

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In reference to claim 14, Hashimoto and Taniguchi disclose all of the claim limitations as applied to claim 7 above. Claim 14 is equivalent in scope to claim 7 and therefore is rejected under similar rationale.

3. Claims 2, 3, 8-10, 12, 13 and 15-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hashimoto (U.S. Patent 5,784,132), Taniguchi (U.S. Patent 4,824,212) and further in view of Hill et al. (U.S. Patent 6,577,291 B2).

In reference to claim 2, Hashimoto and Taniguchi disclose all of the claim limitations as applied to claim 1 above however neither Hashimoto nor Taniguchi explicitly disclose a backlight element for illuminating the matrix of pixels. Hill et al. discloses a color LCD comprising of a light source at the back of the display which provides light to the elements of the display (see column 25, lines 17-41 and prior art #2102 of Figure 21A). Hill et al. also discloses the LCD comprising a backlight source which provides light to RGB color filters to create a desired color for each pixel of the display (see Hill et al. column 25, lines 17-22 and prior art Figure 21A). It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement the dummy pixel display techniques of Hashimoto and the non-display region of the display of Taniguchi with the backlight assembly of Hill et al. in order to create legible characters on a display device by implementing the rendering of, normally illegible characters, varying the illumination of the boundary of the effective display region surrounding the characters (see columns 1-2, lines 66-13 of Taniguchi).

In reference to claims 3, 10 and 17, Hashimoto, Taniguchi and Hill et al. disclose all of the claim limitations as applied to claims 2, 9 and 16 respectively in addition, Hill et al. discloses each pixel to comprise of RGB display elements with each of the display elements having a

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transparent electrode that can be separately controlled (see column 25, lines 42-50 and prior art Figure 21B).

In reference to claim 8, claim 8 is equivalent in scope to claim 1 and therefore is rejected under similar rationale. Further, neither Hashimoto nor Taniguchi explicitly disclose a portable electronic device however Hill et al. does. Hill et al. discloses an LCD display utilized by a portable personal computer (see column 1, lines 55-62 and Figure 1). Hill et al. also discloses a personal computing device to comprise of a processor coupled to a bus (see #521 and 523 of Figure 5), a memory unit couple to the bus (see #522 of Figure 5), a user input device coupled to the bus via a serial port interface (see #540 and 546 of Figure 5) and a display unit coupled to the bus via a video adapter (see #547 and 548 of Figure 5). It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement the dummy pixel display techniques of Hashimoto and the illuminating non-display region of the display of Taniguchi with the computer system of Hill et al. in order to make portable the character contrast techniques as indicated above (see column 1, lines 55-62 of Hill et al.).

In reference to claims 9 and 16, Hashimoto, Taniguchi and Hill et al. disclose all of the claim limitations as applied to claims 8 and 15 respectively. Claims 9 and 16 are equivalent in scope to claim 2 and therefore are rejected under similar rationale.

In reference to claims 12 and 18, Hashimoto, Taniguchi and Hill et al. disclose all of the claim limitations as applied to claims 8 and 15 respectively. Claims 12 and 18 are equivalent in scope to claim 5 and therefore are rejected under similar rationale.



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In reference to claims 13 and 19, Hashimoto, Taniguchi and Hill et al. disclose all of the claim limitations as applied to claims 8 and 15 respectively. Claims 13 and 19 are equivalent in scope to claim 6 and therefore are rejected under similar rationale.

In reference to claim 15, Hashimoto discloses a display device comprising a panel structure housing pixel electrodes laid out in a matrix form (see column 1, lines 14-21 and prior art Figure 8). Hashimoto also discloses a drive substrate which is used to switch the pixel electrodes individually (see column 1, lines 17-21). Note, the office interprets the drive substrate "switching" process to be functionally equivalent to applicant's claimed light modulation (see lines 3-4 of claim 1) as the drive substrate controls the electric position (On/Off) of each pixel. Hashimoto also discloses dummy pixels arranged in positions so as to surround the effective display pixels (see column 5, lines 64-66) which the office interprets as equivalent to a pixel border having a predetermined width as the specific width is a matter of design choice as preferred by the designer and which best suits the application. Hashimoto discloses each dummy pixel made up of the same contents as an effective display pixel however the dummy pixels are not provided with a pixel electrode (#4 of Figure 4) (see column 6, lines 47-48 and Figure 4 where dummy pixel lacks pixel electrode as seen in #4 of right most pixel (effective display pixel)). Note, the pixel electrode of Hashimoto is seen to be functionally equivalent to an active element as the pixel electrode may be switched to illuminate the pixel. Hashimoto does not explicitly disclose the dummy pixels allowing light to pass through to improve contrast of edge-displayed images however Taniguchi does. Taniguchi discloses an LCD device comprising two display region types, a display region and a non-display region (see lines 1-8 of abstract). Taniguchi discloses independently controlling elements of the non-display region, from those in

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the effective display region, to make the non-display region bright or dark (see column 2, lines 58-64). Note, the office interprets the elements making up the non-display region of Taniguchi functionally equivalent to the dummy pixels of applicant's claim. Taniguchi also discloses lighting the non-display region, either bright or dark, to create a distinguishable difference between the effective display and non-display regions (see column 2, lines 65-68). Note, the office interprets such a distinguishable difference functionally equivalent to contrast. It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement the dummy pixel display techniques of Hashimoto with the illuminating non-display region of the display of Taniguchi in order to create legible characters on a display device by implementing the rendering of, normally illegible characters, varying the illumination of the boundary of the effective display region surrounding the characters (see columns 1-2, lines 66-13 of Taniguchi). Neither Hashimoto nor Taniguchi explicitly disclose a portable electronic device however Hill et al. does. Hill et al. discloses an LCD display utilized by a portable personal computer (see column 1, lines 55-62 and Figure 1). Hill et al. also discloses a personal computing device to comprise of a processor coupled to a bus (see #521 and 523 of Figure 5), a memory unit couple to the bus (see #522 of Figure 5), a user input device coupled to the bus via a serial port interface (see #540 and 546 of Figure 5) and a display unit coupled to the bus via a video adapter (see #547 and 548 of Figure 5). It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement the dummy pixel display techniques of Hashimoto and the illuminating non-display region of the display of Taniguchi with the computer system of Hill et al. in order to make portable the character contrast techniques as indicated above (see column 1, lines 55-62 of Hill et al.). Hashimoto, Taniguchi

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and Hill et al. do not explicitly disclose generating an image wherein the image is representative of information stored in a frame buffer memory as a frame buffer is well known in the art to store frames of graphic data, known to make up graphic images or video, that are waiting to be displayed onto a display screen. It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement a frame buffer memory for storing information of an image to be displayed as such a technique is well known in the art (Official Notice). Further note, neither Taniguchi, Hashimoto nor Hill et al. explicitly disclose the dummy pixels being not capable of modification however, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the controlling of dummy pixels in Taniguchi and Hashimoto to prohibit change in dummy pixel output thus making the dummy pixels not capable of modification as such processing could easily be implemented in software, instructing pixel drivers to not change dummy pixel output states. Further, any pixel in the LCD display art can be configured to not be capable of modification if the driving software is configured to not modify the pixel.

In reference to claim 20, Hashimoto, Taniguchi and Hill et al. disclose all of the claim limitations as applied to claim 15 above. Claim 20 is equivalent in scope to claim 7 and therefore is rejected under similar rationale.

4. Claims 4 and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hashimoto (U.S. Patent 5,784,132), Taniguchi (U.S. Patent 4,824,212), Hill et al. (U.S. Patent 6,577,291 B2) and further in view of Sono et al. (U.S. Patent 5,513,028).

In reference to claims 4 and 11, Hashimoto, Taniguchi and Hill et al. disclose all of the claim limitations as applied to claims 3 and 10 respectively above however Hashimoto,

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Taniguchi and Hill et al. do not explicitly disclose the dummy pixels comprising of a red sub-dummy pixel, green sub-dummy pixel and a blue sub-dummy pixel. Sono et al. discloses a liquid crystal display device having display and non-display areas made up of a pixel electrode substrate (see lines 1-3 of abstract). Sono et al. discloses a dummy area of the display, made up of dummy pixels, that has the same configuration as the display area (see column 3, lines 18-21). Sono et al. also discloses each pixel of the display area to comprise of a red, green or blue color filter (see column 3, lines 54-63). It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement the dummy pixel display techniques of Hashimoto, the illuminating non-display region of the display of Taniguchi and RGB pixel component arrangement of Hill et al. with the technique of providing the dummy pixels with an equivalent configuration as the display area pixels, as disclosed by Sono et al., in order to make the process of manufacturing display screens using dummy pixels simple and easy by being able to merely expand the display area and creating dummy pixels from the expanded area (see column 3, lines 23-27 of Sono et al.).

### ***Response to Arguments***

5. Applicant's arguments filed 2/24/2004 have been fully considered but they are not persuasive.

In reference to claims 1, 8 and 15, applicant argues that, "...Taniguchi does not teach nor render obvious a dummy pixel as claimed since Taniguchi teaches controllable pixels," (see page 8 of Applicant's Remarks). Although Taniguchi does disclose controllable pixels, the added limitation to claims 1, 8 and 15 cites, "...dummy pixels...not capable of modification," (see lines 9-10 of claim 1, for example). The office sees such a limitation rendered obvious by Taniguchi

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and Hashimoto as it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the controlling of dummy pixels in Taniguchi and Hashimoto to prohibit change in dummy pixel output thus making the dummy pixels not capable of modification as such processing could easily be implemented in software (well known in the art), instructing pixel drivers to not change dummy pixel output states. Even though the dummy pixels of Taniguchi are controllable, they can be prohibited from being controlled and therefore could not be modified. Further, any pixel in the LCD display art can be configured to not be capable of modification if the driving software is configured to not modify the pixel. Therefore such a limitation does not overcome the cited art, as seen by the office.

### *Conclusion*

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Antonio Caschera whose telephone number is (703) 305-1391. The examiner can normally be reached Monday-Thursday and alternate Fridays between 7:00 AM and 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Bella, can be reached at (703)-308-6829.

**Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks

Washington, D.C. 20231

**or faxed to:**

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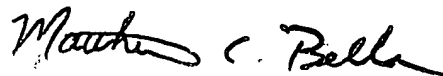
**(703) 872-9314 (for Technology Center 2600 only)**

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive,  
Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding  
should be directed to the Technology Center 2600 Customer Service Office whose telephone  
number is (703) 306-0377.

aac

4/2/04



**MATTHEW C. BELLA  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2600**